

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	693	712/216	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/18 13:03
S2	651	712/217	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/18 13:03
S3	701	712/218	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/18 13:03
S4	471	712/219	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/18 13:04
S5	0	(\$3dependenc\$3 near4 (signal\$1 or indication\$1 or bit\$1)) near4 (buffer near4 address) near4 (active or true or high or set)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/18 15:20
S6	7	(\$3dependenc\$3 near4 (signal\$1 or indication\$1 or bit\$1)) near4 (buffer near4 address)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/18 13:09
S7	22	(\$3dependenc\$3 near4 (signal\$1 or indication\$1 or bit\$1)) with (buffer near4 address)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/18 13:49
S8	51	(\$3dependenc\$3 near4 (signal\$1 or indication\$1 or bit\$1)) same (buffer near4 address)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/18 14:44
S9	1	(out?of?order) near4 (bypass\$3) near4 buffer\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/18 16:04
S10	1	(out?of?order) near4 (bypass\$3 or skip\$4 or jump\$3) near4 buffer\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/18 15:18
S11	0	(\$3dependenc\$3) near4 (buffer near4 address) near4 (active or true or high or set)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/18 15:30

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S12	84	(\$3dependenc\$3) with (buffer near4 address)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/18 15:51
S13	22	(\$3dependenc\$3) near4 (buffer near4 address)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/18 15:51
S14	3	(out?of?order) near4 dependenc\$3 near4 buffer\$3 near4 execut\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/18 16:05
S15	3	(out?of?order) near4 \$3dependenc\$3 near4 buffer\$3 near4 execut\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/18 16:05
S16	6	(out?of?order) with (\$3dependenc\$3 near4 buffer\$3 near4 execut\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/18 16:07
S17	9	(out?of?order) with buffer\$3 with (\$3dependenc\$3 near4 execut\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/18 16:12
S18	114	buffer\$3 with (\$3dependenc\$3 near4 execut\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/18 16:12
S19	0	(temporary near4 buffer\$3) with (\$3dependenc\$3 near4 execut\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/18 16:12
S20	62	((data or operand\$1) near4 buffer\$3) with (\$3dependenc\$3 near4 execut\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/18 16:13
S21	60	((data or operand\$1) near4 buffer\$3) near4 (\$3dependenc\$3 near4 execut\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/18 16:13
S22	70	(register adj1 renam\$3) near4 (dependenc\$3 near4 check\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/24 14:54
S23	70	(register adj1 renam\$3) near4 (dependenc\$3 adj1 check\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/24 14:37

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S24	0	(register adj1 renam\$3) near4 (dependenc\$3 near4 (signal\$1 or indication\$1 or bit\$1 or mark\$3 or sign\$1))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/24 14:38
S25	27	(register adj1 renam\$3) with (dependenc\$3 near4 (signal\$1 or indication\$1 or bit\$1 or mark\$3 or sign\$1))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/24 14:38
S26	230	(register adj1 renam\$3) near4 dependenc\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/31 08:02
S27	19	(register adj1 renam\$3) adj1 dependenc\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/24 14:55
S28	1	("5,497,499").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/24 14:55
S29	20	(register adj1 renam\$3) near4 ((allocat\$3 or designat\$3 or stor\$3) near4 (entr\$3 or location\$3 or point\$3 or plac\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/31 08:10
S30	71	(register adj1 renam\$3) with ((allocat\$3 or designat\$3 or stor\$3) near4 (entr\$3 or location\$3 or point\$3 or plac\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/31 08:10
S31	759	712/216	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/11/25 13:56
S32	722	712/217	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/11/25 13:56
S33	776	712/218	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/11/25 13:57
S34	520	712/219	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/11/25 13:57
S35	0	(score?board\$3) near4 (dependenc\$3 near4 check\$3) near4 (bypass\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/11/25 13:58

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S36	0	((score?board\$3) near4 (dependenc\$3 near4 check\$3)) with ((bypass\$3) near4 pipelin\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/11/25 13:58
S37	0	((score?board\$3) near4 (dependenc\$3 near4 check\$3)) same ((bypass\$3) near4 pipelin\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/11/25 14:19
S38	1	((dependenc\$3 near4 check\$3)) same ((bypass\$3) near4 pipelin\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/11/25 14:00
S39	16	(dependenc\$3 near4 check\$3) same ((bypass\$3) with pipelin\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/11/25 14:19
S40	16	(dependenc\$3 with check\$3) same ((bypass\$3) with pipelin\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/11/25 14:19
S41	0	((score?board\$3) near4 (dependenc\$3 near4 check\$3)) same ((bypass\$3) with pipelin\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/11/25 14:19
S42	0	((score?board\$3) with (dependenc\$3 near4 check\$3)) same ((bypass\$3) with pipelin\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/11/25 14:19
S43	0	((score?board\$3) with (dependenc\$3 with check\$3)) same ((bypass\$3) with pipelin\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/11/25 14:19
S44	0	((score?board\$3) same (dependenc\$3 with check\$3)) same ((bypass\$3) with pipelin\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/11/25 14:19
S45	0	((score?board\$3) same (dependenc\$3 with check\$3)) same ((bypass\$3) same pipelin\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/11/25 14:20
S46	0	((score?board\$3) same (dependenc\$3 same check\$3)) same ((bypass\$3) same pipelin\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/11/25 14:20
S47	25	((scoreboard\$3) same (dependenc\$3 same check\$3)) same ((bypass\$3) same pipelin\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/11/25 14:20

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S48	797	712/216	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/11 15:42
S49	776	712/217	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/11 15:42
S50	833	712/218	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/11 15:42
S51	566	712/219	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/11 15:42

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Register renaming - Wikipedia, the free encyclopedia

Most modern machines do **renaming** by RAM indexing a **map table** with the logical **register** number. Eg P6 did this; future files do this, and have data storage ...

en.wikipedia.org/wiki/Register_renaming - 34k -

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www.BabyChangeNBathe.com

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Renaming Implementation

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Register Rename Map Tables. Register renaming logic maps logical register

names in a program to physical registers. that reside in the machine. ...

homepages.cae.wisc.edu/~mikko/752/hw3_handout.pdf - [Similar pages](#)

[PDF] EE382A Lecture 7: Register Renaming Announcements Impediments to ...

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Register Renaming in the RS6000 FPU. FPU Register Renaming. Map table. 32 x

6. Free List. Pending Target Return Queue. Incoming FPU instructions pass ...

www.stanford.edu/class/ee382a/handouts/L07-rename_4up.pdf - [Similar pages](#)

The Design Space of Register Renaming Techniques

use of merged architectural and **rename register** files and **mapping tables** (R10000,

... **Rename Buffer Layout; Register Mapping Methods; Conclusion; References** ...

doi.ieeecomputersociety.org/10.1109/40.877952 - [Similar pages](#)

Reducing Rename Logic Complexity for High-Speed and Low-Power ...

For example, the **register rename** logic is designed to look up the **map table** for both the logical source operands for each instruction along with updating ...

doi.ieeecomputersociety.org/10.1109/TC.2006.88 - [Similar pages](#)

Instruction control mechanism for a computing system with register ...

4 for the details of the **register renaming** block 300 of FIG. 2. The **register renaming** logic 300 consists of four main functional blocks. A **mapping table** 700 ...

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Real World Technologies - Alpha EV8 (Part 2): Simultaneous Multi ...

Register renaming is a scheme in which the logical registers in an instruction ... This could be physically realized with a single **map table** if the size of ...

www.realworldtech.com/page.cfm?ArticleID=RWT122600000000&p=2 - 16k -

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[PDF] Microsoft PowerPoint - Register Renaming

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Ienne 2002. ConProAv — **Register Renaming**. 13. MIPS R10000: Merged RF with **Mapping Table**. Remark the complexity of the **Mapping Table**: 4-issue processor (